

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,982,988 B2

Page 1 of 1

APPLICATION NO. : 10/044766

DATED : January 3, 2006

INVENTOR(S) : Michael E. Rupp, Ronald D. Olsen and Jon C. Melnik

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 6, line 55, claim 7, insert "wherein the control unit receives a bus signal, a frame start signal, and a bit-clock-in signal." after --fields--

Column 6, line 57, claim 8, delete "wherein the control unit received a bus signal, a frame start signal and a bit-clock-in signal" between --fields-- and --include--

Signed and Sealed this

Twenty-fifth Day of July, 2006



JON W. DUDAS  
Director of the United States Patent and Trademark Office